

Electronic Acknowledgement Receipt

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Title of Invention:	Verilog to C++ language translator
First Named Inventor/Applicant Name:	Ghanashyam A. Bailwal
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Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)
1	Amendment - After Non-Final Rejection	ResponsetoOfficeAction15398US01.pdf	37352	no	14

Warnings:

Information:	
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